

**AMENDMENTS TO THE CLAIMS:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

**Listing of Claims:**

1. (Currently Amended) A plasma display having an address electrode, a scan electrode and a sustain electrode, wherein cells are arranged at intersections of the electrodes, comprising:

a first driver for initializing the cells;

a second driver; and

an address driver to select on-cells and to select off-cells, wherein on-cells are selected by the address driver applying data of a first voltage to the address electrode and the first driver applying a scan pulse of a second voltage to the scan electrode, and off-cells are selected by the address driver applying data of a third voltage to the address electrode and the first driver applying the scan pulse to the scan electrodes, wherein the third voltage is greater than the first voltage, and wherein the first voltage to select the on-cells is one of zero (0)V and a ground voltage GND, and wherein the second voltage is a positive voltage.

2. (Previously Presented) The plasma display of claim 1, wherein the first driver supplies a waveform to the scan electrode and the second driver applies an identical waveform to the sustain electrode.

3. (Currently Amended) The plasma display of claim 2, wherein the first driver and the second driver each simultaneously supply a falling ramp waveform and the first driver and the second driver each simultaneously supply a rising ramp waveform following the falling ramp waveform to either the scan electrode or the sustain electrode.

4. (Previously Presented) The plasma display of claim 1, wherein the first driver supplies a falling ramp waveform and a rising ramp waveform following the falling ramp waveform to the scan electrode, and the second driver supplies a fourth voltage to the sustain electrode, the fourth voltage being a negative voltage.

5. (Previously Presented) The plasma display of claim 4, wherein the second driver comprises a sustain driver for supplying the fourth voltage to the sustain electrode in an address period to select the on-cells and the off-cells.

6. (Previously Presented) The plasma display of claim 3, wherein the falling ramp waveform decreases from a first negative voltage to a second negative voltage, an absolute value of the second negative voltage being greater than an absolute value of the first negative voltage, and wherein the rising ramp waveform increases from the first negative voltage to zero(0)V.

7. (Canceled)

8. (Previously Presented) The plasma display of claim 1, wherein the first driver and the second driver alternately apply a sustain pulse of a fourth voltage to the scan electrode and the sustain electrode to cause a sustain discharge to occur with respect to the on-cells.

9. (Currently Amended) A method of driving a plasma display having an address electrode, a scan electrode and a sustain electrode, wherein cells are arranged at intersections of the electrodes, the method comprising:

initializing the cells;

selecting on-cells by applying data of a first voltage to the address electrode and applying a scan pulse of a second voltage to the scan electrode at a time in which the data of the first voltage is applied to the address electrode, wherein the first voltage to select the on-cells is one of zero (0)V and a ground voltage GND, and wherein the second voltage is a positive voltage; and

selecting off-cells by applying data of a third voltage to the address electrode and applying the scan pulse to the scan electrode at a time in which the data of the third voltage is applied to the address electrode, wherein the second voltage is higher than the first voltage.

10. (Previously Presented) The method of claim 9, wherein the initializing the cells includes supplying an identical waveform to both of the scan electrode and the sustain electrode to make wall charges having an identical polarity accumulate on the scan electrode and the sustain electrode.

11. (Previously Presented) The method of claim 10, wherein the initializing the cells includes supplying simultaneously a falling ramp waveform and a rising ramp waveform following the falling ramp waveform to the scan electrode and the sustain electrode.

12. (Previously Presented) The method of claim 9, wherein the initializing the cells includes:

supplying a falling ramp waveform and a rising ramp waveform following the falling ramp waveform to the scan electrode; and

supplying a fourth voltage synchronized with the rising ramp waveform to the sustain electrode, the fourth voltage being a negative voltage.

13. (Previously Presented) The method of claim 9, further comprising supplying a fourth voltage to the sustain electrode to select the on-cells and the off-cells, in an address period.

14. (Previously Presented) The method of claim 11, wherein the falling ramp waveform decreases from a first negative voltage to a second negative voltage, an absolute value of the second negative voltage being greater than an absolute value of the first negative voltage, and wherein the rising ramp waveform increases from the first negative voltage to zero(0)V.

15. (Canceled)

16. (Previously Presented) The method of claim 9, further comprising alternately applying a sustain pulse of a fourth voltage to the scan electrode and the sustain electrode to cause a sustain discharge to occur with respect to the on-cells.

17-18. (Canceled)

19. (Previously Presented) The plasma display of claim 1, wherein the address driver applies data of the first voltage to the address electrode during a reset period and applies data of the third voltage to the address electrode during an address period, and the first driver applies the scan pulse to the scan electrode during the address period.

20. (Previously Presented) The method of claim 9, wherein the scan pulse of the second voltage to select on-cells is applied during an address period and the scan pulse to select off-cells is applied during the address period.

21. (Previously Presented) The method of claim 1, further comprising creating an address discharge within the selected on-cells when a subsequent sustain voltage is applied during a sustain period.

22. (Previously Presented) The method of claim 21, wherein creating the address discharge includes avoiding an address discharge within the selected off-cells during the sustain period.

23. (Previously Presented) The method of claim 9, further comprising maintaining wall charges within the selected off-cells during a sustain period.

24. (Previously Presented) The method of claim 23, wherein selecting the on-cells and selecting the off-cells occurs during an address period preceding the sustain period.

25. (Previously Presented) A plasma display having an address electrode, a scan electrode and a sustain electrode, wherein cells are arranged at intersections of the electrodes, comprising:

a first driver to initialize the cells by providing a falling ramp waveform and a rising ramp waveform following the falling ramp waveform to the scan electrode during a reset period;

a second driver to apply pulses to the sustain electrode; and

an address driver to select on-cells by applying data of a first voltage to the address electrode during the reset period, and the address driver to select off-cells by applying data of a third voltage to the address electrode during an address period, wherein the first driver applying a scan pulse of a second voltage to the scan electrode when the data of the first voltage is applied to the address electrode, and the first driver applying the scan pulse to the scan electrode when the data of the third voltage is applied to the address electrode during the address period, wherein the third voltage is greater than the first voltage.

26. (Previously Presented) The plasma display of claim 25, wherein the second driver provides a falling ramp waveform and a rising ramp waveform following the falling ramp waveform to the sustain electrode at a same time as the first driver provides the falling ramp waveform and the rising ramp waveform to the scan electrode.

27. (Currently Amended) The plasma display of claim 25, wherein the second driver provides a fourth voltage to the sustain electrode, the ~~forth~~ fourth voltage being a negative voltage.

28. (Previously Presented) The plasma display of claim 27, wherein the fourth voltage is applied during the reset period while the rising ramp waveform is provided on the scan electrode.